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Recommendation to adding a few pages to the thesis mentioning that Security is not the goal of the current research.

- To mention aspects such as timing attacks was not evaluated.
 - When storing bounds next to each other attacks such as hammering DRAM to cause bitflips (Rowhammer attack) is a possibility.

We spoke in detail about the use case of Libc against Kernel modules. He asked me why contiguous malloc was not used to which I mentioned the underlying behavior of it.

Adam recommended for the evaluation section to have scatter plot showing the numbers for multiple runs (This is a bit tricky in our case since we are doing a percentage difference).

- We spoke about the performance runs in detail. For instance what ARM counters are missing from the Morello implementation and how we used to the other performance counters to cover them.
- We spoke about the wide use of Jemalloc but the lack of Academic sources to the Jemalloc implementation. It was good to know as someone working in performance side and Adam coming from the Security side we both faced similar rabbit holes when it came to dealing with finding academic algorithmic references to Jemalloc.
- Adam understood my reasoning on why I could not make certain trivial assumptions about cache misses with Jemalloc with Contiguous memory vs Standard Jemalloc. He recommended that the Thesis should have this (Appreciated that these aspects are considered).

In regards to the timetable he mentioned that it's a good decision sticking to the RISC-V simulator and not focusing on the Uni-kernel experiment. He recommended that to keep slippage time until max February or March 2026 as a hard deadline for experiments (But motivated to finish them by December).